10/044121

U.S. UTILITY Patent Application

PATENT NUMBER and ISSUE DATE

SERIAL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10044121	01/11/2002	257	666	2316	

APPLICANTS: Riley John; Warner Michael; Fjelstad Joseph;

**CONTINUING DATA VERIFIED:

THIS APPLN CLAIMS BENEFIT OF 60/261,059 01/11/2001

**FOREIGN APPLICATIONS VERIFIED:

PG-PUB DO NOT PUBLISH | RESCIND

Foreign priority claimed ☐ yes ☑ no

35 USC 119 conditions met ☐ yes ☑ no

Verified and Acknowledged Examiner's initials

ATTORNEY DOCKET NO

TESSERA 3.0-299

TITLE: Stacked microelectronic assemblies and methods of making same

U.S.DEPT. OF COMM./PAT.& TM-PTO-436L(Rev.12-94)

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NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
3/25/4	Assistant Examiner	Total Claims	Print Claim for O.G 1
ISSUE FEE		DRAWING	
Amount Due: $1330.00	JASMINE CLARK Primary Examiner	Sheets Drwg. 11 / Figs. Drwg. 14	Print Fig. 3A
Date Paid:		Application Examiner 3/1/4	

☐ TERMINAL DISCLAMER

PREPARED FOR ISSUE

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